

Principal Memory System Engineer

Job Description:

Well funded start-up company with advanced non-volatile (spin-transfer torque) STT-RAM memory technology seeking principal memory system engineer.

Responsibilities:

- Lead STT-RAM system application efforts to identify key memory systems where the unique advantages of STT-RAM greatly enhance system performance.
- Contribute to Grandis IP portfolio in memory systems.
- Interface with customers and circuit designers to define STT-RAM product specification.
- Work with back-end foundries for memory chip packaging.
- Work with test engineers to qualify memory products.

Desired Skills:

- Expertise in application of memory, such as DRAM, SRAM, EPROM, and Flash memory, in electronic systems and products.
- Have system-level perspective and ability to grasp the "big picture".
- Familiarity with common memory standards such as DDR, DDR2, graphics applications, and asynchronous products.
- Understanding of memory circuit design and memory system architecture.
- Knowledge of package/board design.
- Self-motivated, good teamwork and communication skills, ability to multi-task and work effectively and independently in fast-paced environment.

Desired Experience:

- Strong background in memory architecture and memory system applications such as cell phones.
- Experience in working closely with memory system customers and memory vendors.
- Experience (> 3 years) in memory systems and products.
- Experience in application of memory, such as DRAM, SRAM, EPROM, and Flash memory, in electronic systems and products.
- Experience in working with common memory standards.

Required Education:

BS or higher in Electrical Engineering, ECE, or Physics.

Compensation:

We offer a very competitive compensation package that includes a stock option plan and a patent bonus program. Employees also receive medical, dental, vision and life insurance fully paid by the company, as well as a 401K plan. This position is located in Milpitas (Silicon Valley), California.