

Memory IC Design Director/Manager/Lead Engineer

Job Description:

Well funded start-up company with advanced non-volatile (spin-transfer torque) STT-RAM technology seeking memory IC design director/manager/lead engineer for product development

Responsibilities:

- Lead/manage circuit design team to design very high performance memory cores and complete chips based on proprietary magnetic memory technology
- Design, simulate and analyze low-power, high-speed, high-density memory circuitry
- Interface with foundry and design partner for the design related tasks
- Propose innovative circuit solutions and optimize circuit design to achieve the best performance for magnetic memory technology
- Support the debug and post-silicon validation

Required Skills:

- Hands-on experience on full chip memory circuit design
- Experience in leading design teams to deliver memory chip/module products including tape out, testing, FA and yield improvement
- Good knowledge of memory design including bitcell and sense amp design, memory problems (SER, stability) and architecture
- Solid knowledge in integrated circuit layout rules
- Self-motivated, good teamwork, ability to multi-task, and work effectively and independently in fast paced environment are desired attributes

Required Experience:

- Strong background in transistor-level analog/digital circuit layout for shipped products
- Experience to work closely with foundry
- Extensive experience (> 10 years) in memory circuit design
- Experience for DRAM design and/or SRAM design is required
- Experience with non-volatile memory circuits is a plus

Required Education:

MSEE, or higher in EE, ECE, Physics.

Compensation:

We offer a very competitive compensation package that includes a stock option plan and a patent bonus program. Employees also receive medical, dental, vision and life insurance fully paid by the company, as well as a 401K plan. This position is located in Milpitas (Silicon Valley), California.