

## PRESS RELEASE

For Immediate Release

### **GRANDIS EXPANDS NEXT-GENERATION MAGNETIC MEMORY PATENT PORTFOLIO**

***Latest Patent Awards Enhance Company's IP Offerings, Further Strengthening its Leadership in STT-RAM Technology***

**Milpitas, CA, July 10, 2007** — Grandis, Inc., today announced that it has expanded its industry-leading spin-transfer torque RAM (STT-RAM) technology portfolio with the addition of six United States-granted patents, bringing its U.S. patent total to 29. The new patents cover STT-RAM bit-cell technology advancements—including bit-cell circuitry integration and the use of novel materials and structures—which are key to enabling denser, less expensive non-volatile memory (NVM) devices that consume less power.

"Leading-edge semiconductor companies facing cost and performance challenges need viable, scalable alternatives to existing memory technologies. The capabilities covered in these patents will allow chipmakers to more efficiently leverage our STT-RAM solutions and achieve their time-to-market goals," said Yiming Huai, Ph.D., Grandis co-founder and chief technology officer. "These patents further build upon our extensive IP and services offerings—in turn, strengthening our efforts to drive commercialization and cost-effective adoption of STT-RAM technology across multiple market segments."

The company's latest group of patents addresses the pivotal challenge of reducing write current in high-density memory cells by incorporating both novel magnetic tunnel junction (MTJ) stacks and new storage-layer materials. The associated reduction in bit-cell switching current by an order of magnitude enables significant improvements in memory power, density and cost, while better circuitry integration and reading-signal optimization technology allow for fast, error-free device operation.

## **About Grandis, Inc.**

Grandis is the pioneer in the development of spin-transfer torque RAM (STT-RAM), a universal and scalable memory solution. Grandis licenses its technology to companies that are developing a variety of products incorporating stand-alone and embedded STT-RAM memory. It offers its licensees a complete range of support services from process installation through qualification. By combining non-volatility and high performance with low-power consumption and low cost, STT-RAM can revolutionize the performance of electronic products in many areas. Grandis was established in 2002, and is headquartered in Silicon Valley, California. Investors include Applied Ventures LLC, Sevin Rosen Funds, Matrix Partners, Incubic and Concept Ventures. Additional information about the company is available on the Internet at [www.grandisinc.com](http://www.grandisinc.com)

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