

## **PRESS RELEASE**

### **For Immediate Release**

#### **GRANDIS INC. ANNOUNCES THE FIRST 300MM MTJ FABRICATION FACILITY IN THE UNITED STATES**

**MILPITAS, Calif., Feb. 5, 2009** — Grandis, Inc., the pioneer in spin transfer torque random access memory (STT-RAM), today announced the first 300-millimeter (mm) magnetic tunnel junction (MTJ) fabrication facility (Fab) in the United States dedicated to STT-RAM memory technology. The recent additions to its MTJ Fab enable Grandis to incorporate STT-RAM into its customers' most advanced semiconductor processes on 300mm wafers. STT-RAM is a next-generation, solid-state memory technology that is dense, fast, non-volatile, and scalable, making it the leading universal memory solution for the 45-nanometer (nm) technology node and beyond.

Grandis' MTJ Fab can now handle both 200mm and 300mm customer wafers. In a multi-million dollar investment, key equipment for depositing and annealing MTJ memory elements, the critical building blocks for STT-RAM, was purchased. Since Grandis' new licensees are often not familiar with fabricating MTJ elements and do not possess the associated fab equipment and know-how, the MTJ Fab's main purpose is enable licensees to incorporate Grandis' MTJ elements into their CMOS wafers at the earliest possible stage in the development cycle, thereby accelerating their STT-RAM development and reducing their time-to-market. The MTJ Fab also enables Grandis to conduct leading-edge R&D on new magnetic materials and MTJ elements targeted at further reducing STT-RAM write current and die size. As these new materials, structures and processes are proven, they are transferred immediately to licensees' production fabs.

Farhad Tabrizi, president and CEO of Grandis, said, "Our customers are looking for STT-RAM to replace existing memory technologies at 45nm and beyond. Since most processing at such advanced technology nodes is done on 300mm wafers, it is vital that we have this additional capability in our Silicon Valley fab. Our goal is to expedite our licensees' and partners' development of high-performance STT-RAM in their own silicon, and we are excited that we

have recently begun prototyping our customers' 300mm wafers in our MTJ Fab."

Grandis has been the world leader in the development of STT-RAM, also known as STT-MRAM or SpinRAM, since 2002. It has pioneered the development of innovative materials and structures to enhance spin-transfer efficiency and reduce STT write current while maintaining thermal stability. It has established a comprehensive patent portfolio in spintronics and STT-RAM technology and a strong technical team with extensive expertise in magnetic thin film and semiconductor memory technology. Grandis licenses its STT-RAM technology to OEMs, semiconductor houses, and wafer fabrication companies who benefit from Grandis' unparalleled experience in STT-RAM technology, including the most optimized materials and process conditions developed in its MTJ Fab. The 200mm and 300mm equipment in Grandis' MTJ Fab also enables licensees to accelerate their STT-RAM development before making major dollar investments of their own.

### **About Grandis, Inc.**

Grandis is the pioneer in the development of spin-transfer torque RAM (STT-RAM), a universal and scalable memory solution. Grandis licenses its technology to companies that are developing a variety of products incorporating stand-alone and embedded STT-RAM memory. It offers its licensees a complete range of support services from process installation through qualification. By combining non-volatility and high performance with low-power consumption and low cost, STT-RAM can revolutionize the performance of electronic products in many areas. Grandis was established in 2002, and is headquartered in Silicon Valley, California. Investors include Applied Ventures LLC, Sevin Rosen Funds, Matrix Partners, Incubic and Concept Ventures. Additional information about the company is available on the Internet at [www.grandisinc.com](http://www.grandisinc.com)

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